7th International Symposium on Devices, Circuits and Systems

organized by

Indian Institute of Engineering Science and Technology, Shibpur (Kolkata), India

in collaboration with

Hiroshima University, Hiroshima, Japan

(https://isdcs.iiests.ac.in)

May 27-30, 2025

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ISDCS 2025 focuses on the recent innovation of Devices, Circuits and Systems to highlight the strong foundation in methodology and the integration of multidisciplinary approaches. The theme for the conference this year is "**Silicon to System: Sustainable Innovation for Secure, Intelligent Computing and Green Energy Solutions**". This theme invites researchers, engineers, and industry leaders to explore novel approaches in hardware-software co-design. This four-day conference comprises of Pre-Conference Workshop on the first day (May 27, 2025), followed by the main conference during the last three days (May 28-30, 2025). The workshop focuses on a detailed discussion of the recent developments in the area of Devices, Circuits and Systems. Original contributions are solicited on topics covered under broad areas such as (but not limited to):

- Device Physics and Modeling
- Nano-Structures Based Devices and Its Application
- Power Electronic Devices, Circuits and Systems
- Photonics and Optoelectronics
- Embedded Devices (Logic + Memory- Memristor Based)
- Beyond CMOS: Nano Devices, Circuits & Hybrid Systems
- Analog / Mixed Signal Circuits &Systems
- Neural Networks & Neuromorphic Circuits and Systems
- Visual Communications & Multimedia Signal Processing
- Internet of Things Circuits and Systems
- Algorithms and Circuit for AI and Machine Learning
- Communications Circuits & Systems
- Design for AI acceleration and machine learning for SoC design

- Cyber Physical System (CPS)
- EDA Methodologies
- Hardware Security
- 3D IC Design and Test
- Biomedical Circuits &Systems
- Digital Circuits &Systems
- Nonlinear Systems & Circuit Theory
- Sensory Circuits &Systems
- Photonics and Renewal Energy
- MEMS/MOEMS
- Test and Verification
- Emerging computing paradigms: Quantum computing, cryogenic processors

Submission link: https://cmt3.research.microsoft.com/ISDCS2025

The ISDCS'25 Program Committee invites original, unpublished paper submissions on the above topics. It is planned to publish the peer reviewed and selected papers of conference as proceedings with Springer in their prestigious "Lecture Notes in Electrical Engineering" series (https://www.springer.com/series/7818). The final camera-ready copy of the papers must be in Springer LNEE format with a length of **maximum 15 pages**. Check Author Guidelines in Springer for formatting the camera-ready article. Selected papers from the conference will be published by Springer as a proceedings book volume. The submission will be considered evidence that upon acceptance the author(s) will submit a final camera-ready version of the paper for inclusion in the proceedings, and will present the paper at the symposium. Each accepted contribution must have at least one full paid registration by the time the camera-ready paper is submitted for inclusion in the proceedings. ISDCS reserves the right to remove from proceedings papers not presented at the symposium.

Key Dates:
Submission deadline : ~~16th March 2025~~ 30th March 2025
Notification of acceptance : 20th April 2025
Camera ready manuscript : 4th May 2025

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Call for Papers

For any query contact: contact.isdcs2025@gmail.com